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**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL NUM 10053489	FILING DATE 11/02/2001	CLASS 257	SUBCLASS 2814	GAU 2814	EXAMINER P. CAO
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****APPLICANTS:** Pasqualini Ronald.

****CONTINUING DATA VERIFIED:**
None, R

**** FOREIGN APPLICATIONS VERIFIED:**
None, R

PG-PUB DO NOT PUBLISH	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO
Foreign priority claimed 35 USC 119 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	NSC1P218/P05033
Verified and acknowledged Examiners initials: <i>PC</i>		
TITLE: Integrated circuit package with low inductance ground path and improved thermal capability		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
ISSUE FEE Amount Due Date Paid		Assistant Examiner	Total Claims Print Claim for O.G.
			DRAWING Sheets Drawg. Figs. Drawg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	Application Examiner
		PREPARED FOR ISSUE	
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